MOSIS Integrated Circuit Fabrication Service

Address:
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Description

The MOSIS Service is a low-cost prototyping service for custom and semi-custom VLSI circuit development. MOSIS provides fast-turnaround fabrication of integrated circuits by subcontracting with major mask, fabrication and assembly vendors. MOSIS runs can support projects designed using the set of generic MOSIS design rules, the wafer fabricator's design rules or the DoD's. MOSIS-compatible standard cell libraries have been loaded onto Cascade, Cadence, Dazix, Mentor, Synopsys and Viewlogic. A wider range of layout tools can be used to generate full custom designs. Designs are typically sent to MOSIS through the Internet in CIF format or on tape in Calma GDSII or MEBES format and parts are shipped to designers via Federal Express. To obtain E-Mail information about MOSIS send a message to MOSIS@MOSIS.EDU in one of the following formats. Take care not to include extra colons in your message or MOSIS will not be able to parse it.

To get general information about MOSIS:

REQUEST: INFORMATION
TOPIC: TOPICS
REQUEST: END

To get MOSIS prices and fab schedule:

REQUEST: INFORMATION
TOPIC: PRICE_SCHED,SCHED
REQUEST: END

To get a MOSIS User Manual:

REQUEST: INFORMATION
TOPIC: USER_MANUAL, MANUAL_UPDATES
REQUEST: END

To get response from MOSIS staff member:

REQUEST: ATTENTION
(your message here)
REQUEST: END

Network Access

MOSIS can be reached via E-mail at mosis@mosis.edu.

Who Can Use MOSIS

MOSIS customers must either provided a purchase order to cover costs or be sponsored by DARPA or NSF. Funding is also available to universities teaching VLSI design classes. For details contact the MOSIS Service at <MOSIS@MOSIS.EDU> or Fax (310) 823-6714.