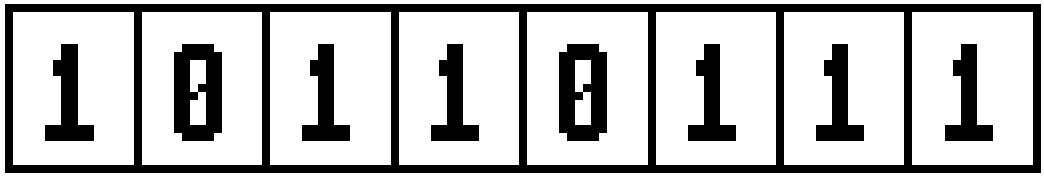
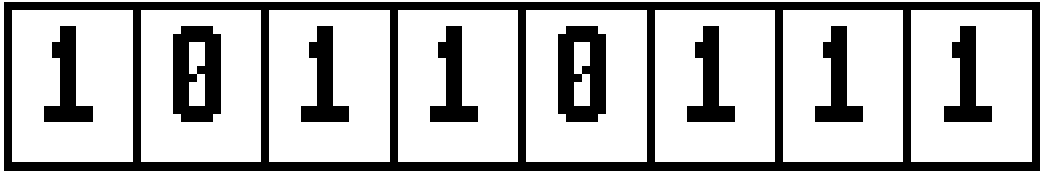
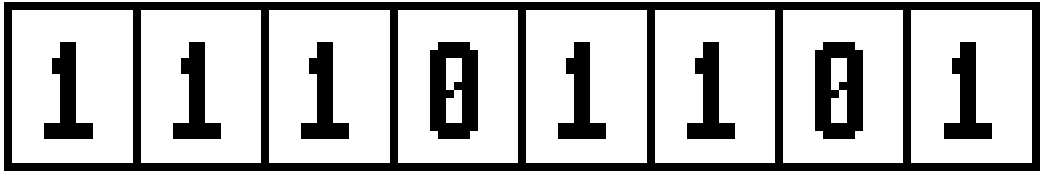
Register



Memory Locations



Method 1



Method 2